

<b>Notice of References Cited</b>	Application/Control No. 10/500,919		Applicant(s)/Patent Under Reexamination RAJU ET AL.	
	Examiner ANNA DENG		Art Unit 2191	Page 1 of 1

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*	B	US-6,640,304 B2	10-2003	Ginter et al.	713/193
*	C	US-6,237,005 B1	05-2001	Griffin, David Michael	707/102
*	D	US-6,199,100 B1	03-2001	Filepp et al.	709/203
*	E	US-6,115,646 A	09-2000	Fiszman et al.	700/181
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#### FOREIGN PATENT DOCUMENTS

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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Barroso, et al. "Piranha: a scalable architecture base on single-chip multiprocessing", ACM, June 2000, ISCA '00, pages 282-293			
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.